



10-31-03

2814

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual  
Capacitance Variation  
Assignee(s): National Semiconductor Corporation

Mountain View, California  
30 Oct 2003

**COMMISSIONER FOR PATENTS**  
**PO Box 1450**  
**Alexandria, Virginia 22313-1450**

**SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENT**

Sir:

In reviewing the Information Disclosure Statement ("IDS") submitted 28 October 2003, it appears that a copy of materials dealing with Japanese Patent Publication ("JPP") 6-61446 was inadvertently enclosed with the 28 October 2003 IDS instead of a copy of materials dealing with JPP 7-226643 listed in the substitute PTO Form 1449 that accompanied, and formed part of, the 28 October 2003 IDS. Accordingly, to complete the 28 October 2003 IDS, enclosed is a copy of JPP 7-226643 along with a copy of an English abstract of JPP 7-226643.

**EXPRESS MAIL LABEL NO.:**

**EV 337 115 733 US**

Ronald J. Meetin  
Attorney at Law  
210 Central Avenue  
Mountain View, CA  
94043-4869  
Tel.: 650-964-9767  
Fax: 650-964-9779

Respectfully submitted,

*Ronald J. Meetin*

Ronald J. Meetin  
Attorney for Applicant(s)  
Reg. No. 29,089

210 Central Avenue  
Mountain View, CA 94043-4869